	Application No.	Applicant(s)
Notice of Allowability	10/766,536 Examiner	CHENG ET AL.
nouse of Anovasiney		Artonic
	Toan Ton	2871
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	(OR REMAINS) CLOSED in this apport or other appropriate communication IGHTS. This application is subject to	plication. If not included will be mailed in due course. THIS
1. This communication is responsive to papers filed 03/17/06 and interview 05/22/06.		
2. The allowed claim(s) is/are <u>1, 3-12, 15-16, 23, 25-34 and 37-38</u> .		
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).		
a) All b) Some* c) None of the:		
1. Certified copies of the priority documents have been received.		
2. Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. ☐ Notice of Informal F	Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. X Interview Summary	(PTO-413),
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Da 08), 7. ⊠ Examiner's Amendi	nent/Comment
Paper No./Mail Date 4.	8. X Examiner's Statement	ent of Reasons for Allowance
3. Diological Material	9.	

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Steven Koffs on 05/22/06.

The application has been amended as follows: nonelected claims 17-22 with traverse have been canceled.

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REASONS FOR ALLOWANCE

2. Claims 1, 3-12, 15-16, 23, 25-34 and 37-38 are allowed.

The following is an examiner's statement of reasons for allowance: the prior art of record does not anticipate nor render obvious to one ordinary skilled in the art a method of bonding integrated circuit chips and other devices to a liquid crystal display panel comprising a combination of various steps as claimed, more specifically, compressing the one of the first integrated circuit chip and the other device together with the panel, placing the other one of the first integrated circuit chip and the other device on a second area portion of the film, compressing the other one of the first integrated circuit chip and the other device together, wherein the first integrated circuit chip is electrically connected with the other device through the anisotropic conductive film extending continuously from the first integrated circuit chip to the other device.

Kawasaki('400)/Takahashi('123) disclose a method of bonding integrated circuit chips and other devices to an LCD panel comprising steps of forming integrated circuit chip, forming anisotropic conductive film, forming other driving devices. However, the prior art of record fails to disclose a method of bonding integrated circuit chips and other devices to an LCD panel comprising a combination of various steps as claimed, more specifically, 'compressing the one of the first integrated circuit chip and the other device together with the panel, placing the other one of the first integrated circuit chip and the other device on a second area portion of the film, compressing the other one of the first integrated circuit chip and the other device together, the first integrated circuit chip is electrically connected with the other device through the anisotropic conductive film extending continuously from the first integrated circuit chip to the other device'.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

CONTACT INFORMATION

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Ton whose telephone number is (571) 272-2303.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

May 22, 2006

TONTON PENARY EXAMPLES